

## CLAIMS:

1. Receiver comprising:
  - a first input for receiving a first signal;
  - a second input for receiving a second signal;
  - a switching circuit with at least two first switches to be activated in a first
  - 5 mode and with at least two second switches to be activated in a second mode; and
  - a control circuit for controlling the switching circuit,
  - characterized in that at least one of the first switches and at least one of the
  - second switches together isolate at least one of the inputs from the other, with the control
  - circuit supplying a first control signal to the first switches and supplying a second control
  - 10 signal to the second switches.
2. Receiver according to claim 1, characterized in that the first switches are diodes of which the anodes are coupled to the first input via one or more elements, with the second switches being diodes of which the cathodes form a common point.
- 15 3. Receiver according to claim 2, characterized in that the cathode of the one of the first switches is coupled to the common point and that the cathode of the other one of the first switches is coupled to a filter-input via one or more elements, with an anode of one of the second switches being coupled to the second input via one or more elements and with an
- 20 anode of the other one of the second switches being coupled to the filter-input via one or more elements.
4. Receiver according to claim 3, characterized in that the receiver comprises a low band filter comprising the filter-input for low band filtering television signals and radio
- 25 signals.
5. Receiver according to claim 4, characterized in that the control circuit comprises a third switch of which a first contact is coupled to a common point of a first serial circuit of two resistors of which the non-common points are coupled to the anodes of the first

switches, with the control circuit further comprising a fourth switch of which a first contact is coupled to a second contact of the third switch and to a common point of a second serial circuit of two resistors of which the non-common points are coupled to the anodes of the second switches, with the common point of the second switches being coupled to ground via a resistor.

6. Receiver according to claim 5, characterized in that the third switch comprises a first transistor and the fourth switch comprises a second transistor, with first contacts of the transistors being collectors, with second contacts of the transistors being bases, and with emitters of the transistors being coupled to ground, with the collectors each via at least one resistor being coupled to a first voltage source and with the basis of the first transistor being coupled to the collector of the second transistor via a resistor and with a basis of the second transistor being coupled to a second voltage source via at least one resistor.

7. Switching circuit for use in a receiver comprising a first input for receiving a first signal, a second input for receiving a second signal, and the switching circuit with at least two first switches to be activated in a first mode and with at least two second switches to be activated in a second mode, characterized in that at least one of the first switches and at least one of the second switches together isolate at least one of the inputs from the other.

8. Control circuit for use in a receiver comprising a first input for receiving a first signal, a second input for receiving a second signal, a switching circuit with at least two first switches to be activated in a first mode and with at least two second switches to be activated in a second mode, and the control circuit for controlling the switching circuit, characterized in that at least one of the first switches and at least one of the second switches together isolate at least one of the inputs from the other, with the control circuit supplying a first control signal to the first switches and supplying a second control signal to the second switches.

9. Television comprising a receiver comprising a first input for receiving a first signal and comprising a second input for receiving a second signal and comprising a switching circuit with at least two first switches to be activated in a first mode and with at least two second switches to be activated in a second mode and comprising a control circuit for controlling the switching circuit, characterized in that at least one of the first switches and at least one of the second switches together isolate at least one of the inputs from the other,

with the control circuit supplying a first control signal to the first switches and supplying a second control signal to the second switches.

10. Method comprising the steps of:

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at a first input receiving a first signal;  
at a second input receiving a second signal;  
activating at least two first switches in a first mode;  
activating at least two second switches in a second mode; and  
controlling the switching,

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characterized in that at least one of the first switches and at least one of the second switches together isolate at least one of the inputs from the other, with the step of controlling comprising the steps of supplying a first control signal to the first switches and of supplying a second control signal to the second switches.